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Receipt

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of

Shunpei YAMAZAKI et al.

Serial No. 09/239,948

Filed: January 29, 1999

For: A MEMORY DEVICE HAVING A
FLOATING GATE

) Issue Branch

) Confirmation No. 6729

) Art Unit: 2811

) Examiner: G. Munson

)

)

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SECOND REQUEST FOR CORRECTED FILING RECEIPT

Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

Further to the Request for Corrected Filing Receipt filed November 5, 2001, applicants respectfully request that the Filing Receipt be corrected as shown on the attached copy.

Respectfully submitted,

By 
Eric J. Robinson
Reg. No. 38,285

NIXON PEABODY, LLP
8180 Greensboro Drive, Suite 800
McLean, Virginia 22102
Telephone: (703) 790-9110
Facsimile: (703) 883-0370



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/239,948	01/29/1999	2811	4498	0756-1930	8	16	8

CONFIRMATION NO. 6729

CORRECTED FILING RECEIPT



OC000000006803343

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2010 CORPORATE RIDGE
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MCLEAN, VA 22102

Date Mailed: 09/27/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

SHUNPEI YAMAZAKI, TOKYO, JAPAN;
YASUHIKO TAKEMURA, KANAGAWA, JAPAN;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A DIV OF 08/847,314 04/23/1997 PAT 5,965,915
AND A DIV OF 08/692,227 08/07/1996 PAT 5,789,292
WHICH IS A DIV OF 08/355,652 12/14/1994 PAT 5,569,615
WHICH IS A DIV OF 08/142,048 10/28/1993 PAT 5,541,138RECEIVED
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Foreign Applications

JAPAN 4-316138 10/30/1992

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If Required, Foreign Filing License Granted 09/27/2001

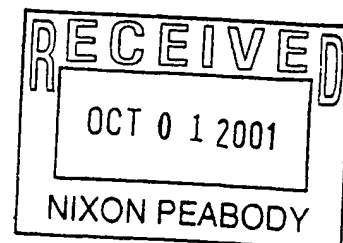
Projected Publication Date: Not Applicable, filed prior to November 29, 2000

Non-Publication Request: No

Early Publication Request: No

Title

LASER PROCESSING METHOD, METHOD FOR FORMING A FLASH MEMORY, INSULATED

PM
10/11/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:)
Shunpei YAMAZAKI et al.) Attn: Customer Correction Branch
Serial No. 09/239,948)
Filed: 01/29/1999)
For: LASER PROCESSING METHOD, METHOD)
FOR FORMING A FLASH MEMORY, INSULATED)
GATE SEMICONDUCTOR DEVICE AND METHOD)
Attention: Application Processing Division

REQUEST FOR CORRECTED FILING RECEIPT

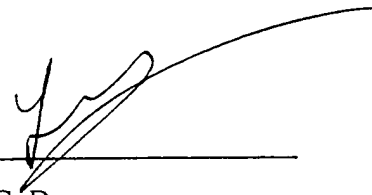
Honorable Commissioner of Patents
Washington, D.C. 20231

Sir:

The Filing Receipt received in the above-identified application has errors thereon due to Patent Office error as shown in red on the attached copy.

In view of the above, it is requested that a Corrected Filing Receipt be issued.

Respectfully submitted,



Luan C. Do
Registration No. 38,434

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CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the U. S. Patent and Trademark Office on the date shown below.

Eric J. Robinson

Name of Person Signing Certification

Signature

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